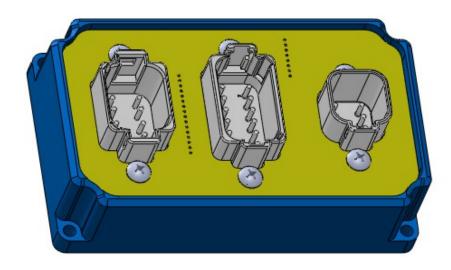
Jack Leg I/O



The device is designed for mobile applications and features robust packing, wide temperature and voltage tolerance as well as strong EMI and ESD immunity.

It features an ARM micro-controller running the 621 co-operative multi-tasking kernel to provide a light high performance solution with start-up times as low a couple of Milliseconds.

Protocols include Generic CAN or J1939.

Part Number	AECU-JCTR-101	JACK LEG I/O

ECU-JCTR-101		
Environmental		
0°C to 85°C		
Electrical		
Communication		